

## **LEADER TIME SRL**

## PRODUCT SPECIFICATION

## 16\*2 Characters COB LCD MODULE MODEL: LT-1602B1-261 Ver:1.0

< >> Finally Specification

	CUSTOMER'S APPROVAL				
CUSTOMER:					
SIG	NATURE:	DATE:			

APPROVED	PM	PD	PREPARED
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# Prepared By: LEADER TIME SRL VIA MONS. PROSDOCIMI, 27

36042 BREGANZE (VI)

I This specification is subject to change without notice. Please contact LT or it's representative before designing your product based on this specification.

Issued Date:2010.7.30 Doc. No.:

## **Revision Status**

Version	Revise Date	Page	Content	Modified By
Ver1.0	2010-7-30		First Issued	

2/18

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## 1. FEATURES

#### The features of LCD are as follows

\* Display mode : STN/ Yellow-Green/Transflective/Positive

\* Controller IC :AIP31066(English-Japanese)

\* Display format : 16\*2 Characters

\* Interface : 4-Bit or 8-Bit MPU

\* Driving Method : 1/16Duty, 1/5Bias

\* Viewing Direction : 12 O'clock

\* Backlight : LED Yellow-Green(Bottom)

\*Sample NO. : EC1602E9SBY7B-A0\_01/20100724

## 2. MECHANICAL SPECIFICATIONS

Item	Specification	Unit
Module Size	84(W) x 44(H) x 12.8MAX(D)	mm
View display area	61(W) x16(H)	mm
Activity Display Area	56.21(W) x 11.5 (H)	mm
Character Font	5x8 Dots	-
Character Size	2.95(W) x 5.55(H)	mm
Character Pitch	3.55(W) x 5.95(H)	mm
Dots Size	0.55(W) x0.65(H)	mm

## 3. ELECTRICAL SPECIFICATIONS

#### 3-1 ABSOLUTE MAXIMUM RATINGS (Ta = 25 °C)

Item	Symbol	Sta	11!1		
item	Symbol	Min.	Тур.	Max.	Unit
Supply Voltage For Logic	VDD - VSS	-0.3	ı	7.0	V
Supply Voltage For LCD Drive	$V_{LCD}$	VDD -15	-	VDD +0.3	V
Input Voltage	Vin	-0.3	-	VDD+0.3	V
Operating Temp.	Тор	-20	-	+70	°C
Storage Temp.	Tst	-30	-	+80	°C

<sup>\*.</sup> NOTE: The response time will be extremely slow when the operating temperature is around -10°C, and the back ground will become darker at high temperature operating.

### 3-2 ELECTRICAL CHARACTERISTICS

Item		Symbol	Test Condition	Min.	Тур.	Max.	Unit
Logic supply	Voltage	VDD - Vss		4.5	5	5.5	٧
LCD Drive V	oltage	V <sub>LCD</sub>		3.76	4.06	4.36	V
	"H" Level	V <sub>IH</sub>	Ta = 25 °C	2.2	-	VDD	V
Input Voltage	"L" Level	V <sub>IL</sub>	VDD=5V ± 10%	-0.3	-	0.6	V
Frame Freq	uency	f <sub>FLM</sub>		1	84.3	ı	Hz
Current Cons	umption	I <sub>DD</sub>		1	1.33	-	mA

### 3-3 BACKLIGHT

3-3-1. Absolute Maximum Ratings

Item	Symbol	Condition	min	Тур	Max	Unit
Forward Current	IF		-	-	110	mA
Reverse Voltage	VR	Ta = 25 °C	-	-	10	V
Power Dissipation	PD		-	-	462	mW

3-4-2. Electrical-optical Characteristics

Item	Symbol	Condition	min	Тур	Max	Unit
Forward Voltage	VF	If 440 A	3.8	4.2	4.6	V
Average Luminous Intensity	lv	If=110mA Ta = 25 °C	30	-	-	cd/m <sup>2</sup>
Peak emission wavelength	λР		-	570	-	nm

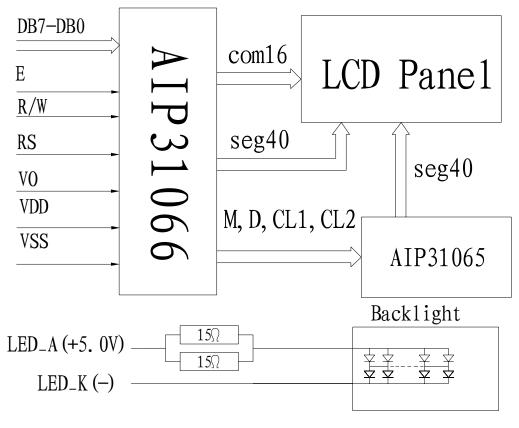
The brightness is measured without LCD panel

## **4.TERMINAL FUNCTIONS AND BLOCK DIAGRAM**

#### 4-1. INTERFACE PIN FUNCTION DESCRIPTION

PIN NO.	SYMBOL	FUNCIONS
1-8	DB7~DB0	8 Bit Data Bus
9	E	A enable signal for reading or writing data.
10	R/W	A signal for selecting read or write actions.1: Read, 0: Write.
11	RS	A signal for selecting registers.  1: Data Register (for read and write)  0: Instruction Register (for write)
12	V0	Supply voltage for LCD driving
13	VDD	Supply voltage for logical circuit
14	VSS	Ground
15	LED_K	Backlight (-)
16	LED_A	Backlight (+5.0V)

#### 4-2. BLOCK DIAGRAM



CIRCUIT DIAGRAM

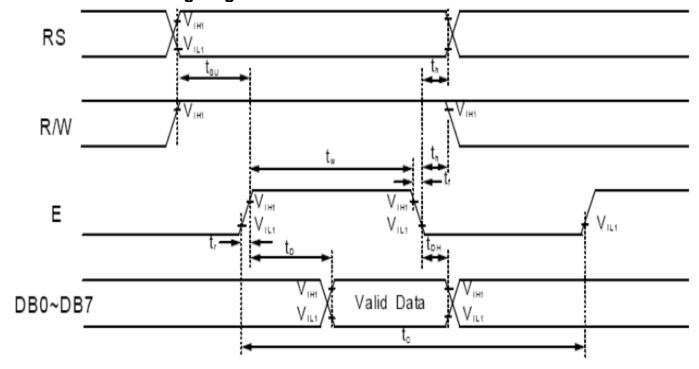
(LED DICE 2×11=22 DIES)

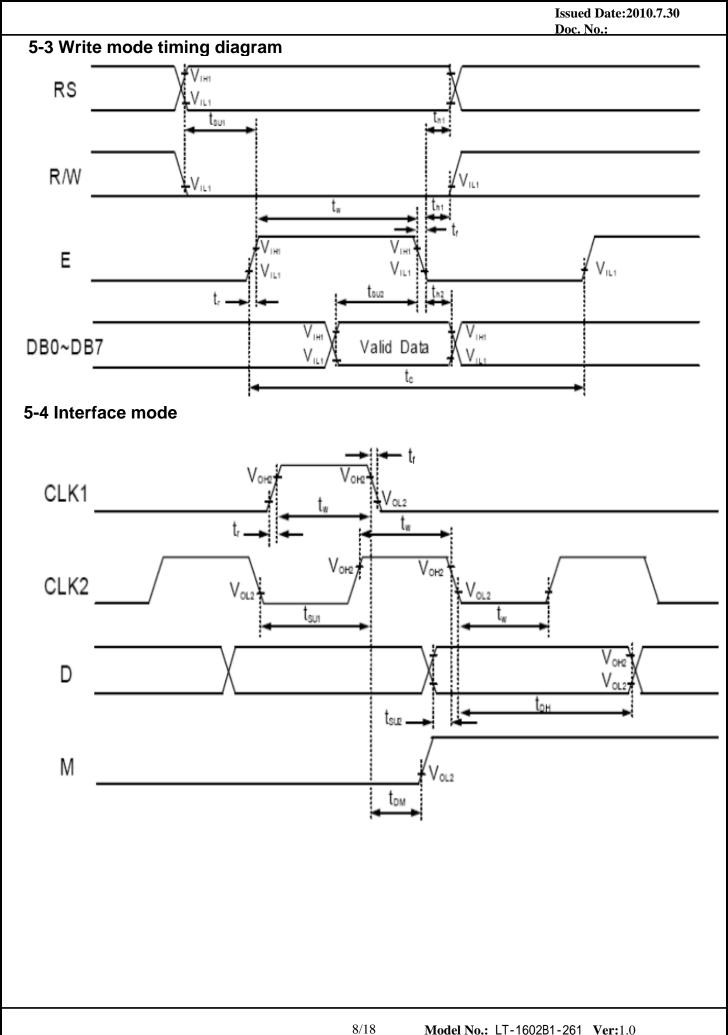
## **5. TIMING CHARACTERISTICS**

## 5-1 Read/Write mode

Mode	Characteristic	Symbol	Min.	Тур.	Max.	Unit
	E Cycle Time	tc	500	23	21	
	E Rise / Fall Time	$t_{\rm R},t_{\rm F}$	5	20	20	
	E Pulse Width (High, Low)	t <sub>w</sub>	230	5		
Write Mode (Refer to Fig-1)	R/W and RS Setup Time	t <sub>su1</sub>	40	- 40	-	ns
(reserve in ing i)	R/W and RS Hold Time	t <sub>H1</sub>	10	20.	. M	
	Data Setup Time	t <sub>su2</sub>	80	· .	- 1	
	Data Hold Time	t <sub>H2</sub>	10	40	-	
	E Cycle Time	t <sub>c</sub>	500	<u>.</u> 91	, y	
	E Rise / Fall Time	t <sub>R</sub> , t <sub>F</sub>	-	<del>-</del> 0	20	
March 10 Ann (10 Ann 10 A	E Pulse Width (High, Low)	t <sub>w</sub>	230	- 40		
Read Mode (Refer to Fig-2)	R/W and RS Setup Time	t <sub>su</sub>	40	23	21	ns
(Neier to Fig-2)	R/W and RS Hold Time	t <sub>H</sub>	10	50	51	
	Data Output Delay Time	t <sub>o</sub>	-	40	120	
	Data Hold Time	t₀н	5	26	P.S.	

## 5-2 Read mode timing diagram





## **6. COMMAND LIST**

Instruction				Inst	ructi	on C	ode				Description	Execution time (fosc=
instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description	270 kHz)
Clear Display	0	0	0	0	0	0	0	0	0	1	Write "20H" to DDRAM and set DDRAM address to "00H" from AC	1.53 ms
Return Home	0	0	0	0	0	0	0	0	1	-	Set DDRAM address to '00H" from AC and return cursor to its original position if shifted. The contents of DDRAM are not changed.	1.53 ms
Entry Mode Set	0	0	0	0	0	0	0	1	I/D	SH	Assign cursor moving direction and enable the shift of entire display.	39 µs
Display ON/ OFF Control	0	0	0	0	0	0	1	D	С	В	Set display(D), cursor(C), and blinking of cursor(B) on/off control bit.	39 µs
Cursor or Display Shift	0	0	0	0	0	1	S/C	R/L	-	-	Set cursor moving and display shift control bit, and the direction, without changing of DDRAM data.	39 µs
Function Set	0	0	0	0	1	DL	N	F	-	-	Set interface data length (DL: 8-bit/4-bit), numbers of display line (N: 2-line/1-line) and, display font type (F:5×11dots/5×8 dots)	39 µs
Set CGRAM Address	0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0	Set CGRAM address in address counter.	39 μs
Set DDRAM Address	0	0	1	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Set DDRAM address in address counter.	39 µs
Read Busy Flag and Address	0	1	BF	AC6	AC5	AC4	AC3	AC2	AC1	AC0	Whether during internal operation or not can be known by reading BF. The contents of address counter can also be read.	0 μs
Write Data to RAM	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write data into internal RAM (DDRAM/CGRAM).	43 μs
Read Data from RAM	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read data from internal RAM (DDRAM/CGRAM).	43 μs

\* "-". dont care

NOTE: When an MPU program with checking the Busy Flag(DB7) is made, it must be necessary 1/2Fosc is necessary for executing the next instruction by the falling edge of the 'E' signal after the Busy Flag (DB7) goes to "Low".

## 7.CHARACTER GENERATOR ROM

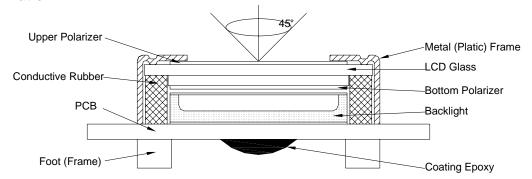
Upp # 4 Lewer Bits 4 Bib	00 00	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	11 00	1101	11 10	1111
xxxx0000	CG RAM (1)			0	9	P	•	F				_	9	Ę	Ċζ	þ
xxxx0001	(2)		!	1	A	Q	a	9				7	Ŧ	4	Ù	q
xxxx0010	(3)			2	В	R	b	٣				1	ij	×	ß	0
xxxx0011	(4)		#	3	C	5	C	5			7	<del>'</del>	Ť	ŧ	ω	80
xxxx0100	(5)		\$	4	D	Ţ	d	ŧ.				I	ķ	þ	4	Э
xxxx0101	(6)		7	5	E	U	e	u				7	<b>†</b>	1	5	ü
xxxx0110	(7)		8	6	F	Ų	f	Ų			₽	Ħ	_	3	ρ	Ы
xxxx0111	(8)		7	7	G	Ŵ	9	W			7	<b>‡</b>	Z	<b></b>	9	π
xxxx1000	(1)		(	8	H	X	h	X			4	7	*	IJ	<b>,</b>	X
xxxx1001	(2)		ን	9	I	Y	i	y			þ	<u>ጎ</u>	Į	ιŀ	-1	Ч
xxxx1010	(3)		ķ	=	J	Z	j	Z			I		'n	ŀ	j	Ŧ
xxxx1011	(4)		+	;	K		k	{			#	Ħ	E		X	Ħ
xxxx1100	(5)		7	<		¥	1				Ť	Ð	7	7	4	Ħ
xxxx1101	(6)		_	=	M	]	M	}			<b>ユ</b>	7	$\gamma_{i}$	_	Ł	
xxxx1110	(7)		•	>	N	۸	n	<b>+</b>			3	ţ	ħ		ñ	
xxxx1111	(8)		/	?	0	_	0	+			2	y	₹		ő	

Note: The user can specify any pattern for character-generator RAM.

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## 8. QUALITY SPECIFICATIONS

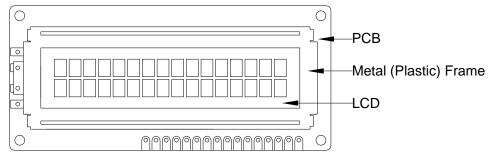
- 8 1. LCM Appearance and Electric inspection Condition
  - 1. Inspection will be done by placing LCM 30cm away from inspector's eyeballs under normal illumination.



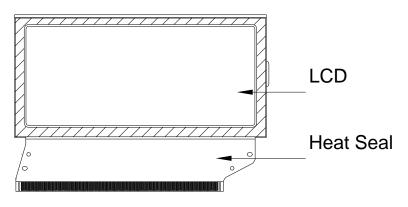
2. View Angle: with in 45° around perpendicular line.

#### 8-2. Definition

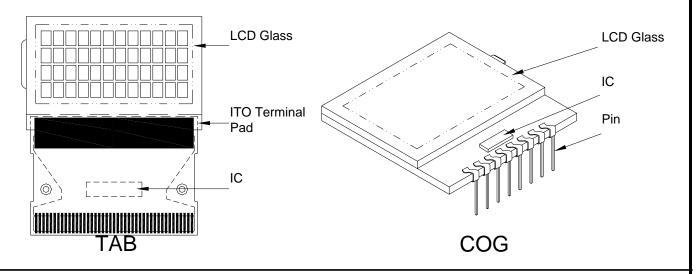
1. COB



2. Heat Seal



#### 3. TAB and COG



## 8-3. Sampling Plan and Acceptance

1.Sampling Plan

MIL - STD - 105E ( || ) ordinary single inspection is used.

2.Acceptance

Major defect: AQL = 0.25%Minor defect: AQL = 0.65%

#### 8-4. Criteria

#### 1.COB

Defect	Inspection Item	Inspection Standards						
Major	PCB copper flakes peeling off	Any copper flake in viewing Area should be greater than 1.0mm <sup>2</sup>	Reject					
Major	Height of coating epoxy	Exceed the dimension of drawing	Reject					
Major	Void or hole of coating epoxy	Expose bonding wire or IC	Reject					
Major	PCB cutting defect	Exceed the dimension of drawing	Reject					

#### 2.SMT

Defect	Inspection Item	Inspection Standa	ards
Minor	Component marking not readable		Reject
Minor	Component height	Exceed the dimension Of drawing	Reject
Major	Component solder defect (missing, extra, wrong component or wrong orientation		Reject
Minor	Component position shift component soldering pad	X < 3/4Z Y > 1/3D	Reject Reject
Minor	Component tilt component soldering pad	Y > 1/3D	Reject
Minor	Insufficient solder component PAD	<i>θ</i> ≤ 20°	Reject

### 3. Metal (Plastic) Frame

Defect	Inspection Item	li	Inspection Standards			
Major	Crack / breakage	Any	/where	Reject		
		W	L	Acceptable of Scratch		
		w<0.1mm	Any	Ignore		
		0.1 <u>&lt;</u> w<0.2mm	L <u>&lt;</u> 5.0mm	2		
Minor	Frame Scratch	0.2 <w<0.3mm< td=""><td>L<u>&lt;</u>3.0mm</td><td>1</td></w<0.3mm<>	L <u>&lt;</u> 3.0mm	1		
		w <u>&gt;</u> 0.3mm	Any	0		
		Note: 1. Above criteria applicable to scratch line with distance greater than 5mm.  2. Scratch on the back side of frame (no visible) can be ignored.				
				Acceptable of Dents / Pricks		
		Φ<	2			
	Frame Dent , Prick	1.0< ₫	1			
Minor	$\Phi = \frac{L + W}{2}$	1.5	0			
	2	/ pricks with dis	e criteria applicable tance greater than rick on the back s ignored	5mm		
Minor	Frame Deformation	Exceed the dimension of drawing				
Minor	Metal Frame Oxidation		Any rust			

#### 4. Flexible Film Connector (FFC)

4. Flexible i	Film Connector (FFC)					
Defect	Inspection Item	Inspection Standards				
Minor	Tilted soldering Within the angle +5°		Acceptable			
Minor	Uneven solder joint /bump		Reject			
		Expose the conductive line	Reject			
Minor	Hole $\Phi = \frac{L + W}{2}$	Φ > 1.0mm	Reject			
Minor	Position shift	Y > 1/3D	Reject			
WIINOT	X	X > 1/2Z	Reject			

#### 5. Screw

Defect	Inspection Item	Inspection Standards	
Major	Screw missing/loosen		Reject
Minor	Screw oxidation	Any rust	Reject
Minor	Screw deformation	Difficult to accept screw driver	Reject

## 6. Heatseal 、TCP 、FPC

Defect	Inspection Item	Inspection Standards	
Major	Scratch expose conductive layer		Reject
Minor	HS Hole $\Phi = \frac{L + W}{2}$	Φ> 0.5mm	Reject
Major	Adhesion strength	Less than the specification	Reject
Minor	Position shift	Y > 1/3D	Reject
IVIII IOI	T X	X > 1/2Z	Reject
Major	Conductive line break		Reject

7. LED Backing Protective Film and Others

Defect	Inspection Item	Inspection Standards				
		Acceptable number of units				
		⊕ <u>&lt;</u> 0.10mm				
		0.10<⊕ <u>&lt;</u> 0.15mm	2			
Minor	LED dirty, prick	LED dirty, prick $0.15 < \Phi \le 0.2 \text{mm}$ $\Phi > 0.2 \text{mm}$				
		The distance between any two spots should be ≥ Any spot/dot/void outside of viewing area is acce				
Minor	Protective film tilt	Not fully cover LCD Re				
Major	COG coating	Not fully cover ITO circuit Reje				

8. Electric Inspection

Defect	Inspection Item	Inspection Standards	
Major	Short		Reject
Major	Open		Reject

## 9. Inspection Specification of LCD

Defect	Insp	ect Item	Inspection Standards							
		* Glass Scratch	W			0.03	0.0	0.0 <u><!--</u--></u>	5 V	V>0.05
		* Polarizer Scratch	L		L.	<5		L<3		Any
Minor	Linear Defect	* Fiber and Linear	ACC. NO.	1		1 1		Reject		
		material	Note	L is the length and W is the width of the de				fect		
		* Foreign material		Φ <u>&lt;</u> 0		0.1<⊕ <u>&lt;</u> 0	.15 (	).15<⊕ <u>&lt;</u> 0.	.2	Φ>0.2
	Black Spot and			3EA 100m	m <sup>2</sup>	2		1		0
Minor	Polarizer Pricked	and glass  * Polarizer hole or protuberance by external force	Note	Distance between two defects > 10mm.						
		* Unobvious			Φ <u>&lt;</u> (	0.3	0.3	<⊕ <u>&lt;</u> 0.5	0.	5<⊕
	White Spot	transparant foreign material between	NO.	3EA	. / 10	00mm <sup>2</sup>		1		0
Minor	and Bubble in polarizer	and Bubble in glass and polarizer				-	erage diameter of the defect. etween two defects > 10mm.			
			Φ	Φ <u>&lt;</u> 0.	Φ <u>&lt;</u> 0.10 0.10<Φ <u>&lt;</u> 0		0.20   0.20<⊕ <u>&lt;</u> 0.25		Φ>0.25	
		<u>w</u>	ACC. NO.	3EA 100m	/ m²	2		1	1 0	
Minor	Segment Defect	- W-		W is m	ore	than 1/2 s	egme	nt width		Reject
	Beleat		Note	$\Phi = \frac{L + W}{2}$ Distance between two defect is 10mm						
			Φ	Φ <u>&lt;</u> 0.10 0.10<Φ <u>&lt;</u> 0		<0.20 0.20<⊕<0.25		0.25	Φ>0.25	
	Protuberant	w W	W	Glue	Glue		W <u>&lt;</u> 1/2 Seg W <u>&lt;</u> 0.2		W <u>&lt;</u> 1/2 Seg W≤0.2	
Minor	Segment	$\Phi = (L + W)/2$	ACC. NO.	3EA 100m	3EA / 100mm <sup>2</sup>		1			0
			1. Seg	ment						
			E	3	B <u>&lt;</u>	<u>0.4mm</u>	0.4 <e< td=""><td>3<u>&lt;</u>1.0mm</td><td>B&gt;1</td><td>I.0mm</td></e<>	3 <u>&lt;</u> 1.0mm	B>1	I.0mm
Minor	Assembly		B-	-A	B-	A<1/2B	В-	A<0.2	B-A	<0.25
	Mis-alignment		Juc	Judge Accep		ceptable	ceptable Acceptable Acce			eptable
			2. Dot Matrix						,	
			Deformation>2°					Reject		
Minor	Stain on LCD Panel Surface		Accept when stains can be wiped lightly with a soft cloth or a similar one. Otherwise, judged according to the above items: "Black spot" and "White Spot"							

## 9. RELIABILITY

NO.	Item	Condition	Criterion		
1	High Temperature Operating	70°C, 96Hrs			
2	Low Temperature Operating	-20℃, 96Hrs			
3	High Humidity	60°C, 90%RH, 96Hrs			
4	High Temperature Storage	80°C, 96Hrs			
5	Low Temperature Storage	-30℃, 96Hrs	No defect in cosmetic and		
		Random wave	operational function allowable.		
6	Vibration	10 ~ 100Hz	Total current Consumption should be below double of initial value.		
0	Vibration	Acceleration: 2g			
		2 Hrs per direction(X,Y,Z)			
		-20℃ to 25℃ to 70℃			
7	Thermal Shock	(60Min) (5Min) (60Min)			
		16Cycles			
		Contract Discharge Voltage:			
8	ESD Testing	+1 ~ 5kV and -1 ~ -5kV	There will be discharged ten times at every discharging voltage		
	J	Air Discharge Voltage: +1 ~ 8kV and -1 ~ -8kV	cycle. The voltage gap is 1kV.		

Note: 1) Above conditions are suitable for our company standard products.

2) For restrict products, the test conditions listed as above must be revised.

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## 10. HANDLING PRECAUTION

(1) Mounting Method

The panel of the LCD Module consists of two thin glass plates with polarizers which easily get damaged since the Module is fixed by utilizing fitting holes in the printed circuit board.

Extreme care should be taken when handling the LCD Modules.

(2) Caution of LCD handling & cleaning

When cleaning the display surface, use soft cloth with solvent (recommended below) and wipe lightly.

- Isopropyl alcohol
- Ethyl alcohol
- Trichloro trifloro thane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use the following solvent:

- Water
- Ketone
- Aromatics
- (3) Caution against static charge

The LCD Module use C-MOS LSI drivers, so we recommend that you connect any unused input terminal to VDD or VSS, do not input any signals before power is turned on. And ground your body, Work/assembly table. And assembly equipment to protect against static electricity.

#### (4) Packaging

- Modules use LCD elements, and must be treated as such. Avoid intense shock and falls from a height.
- To prevent modules from degradation. Do not operate or store them exposed directly to sunshine or high temperature/humidity.

#### (5) Caution for operation

- It is indispensable to drive LCD's within the specified voltage limit since the higher voltage than the limit shorten LCD life. An electrochemical reaction due to direct current causes LCD deterioration. Avoid the use of direct current drive.
- Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark color in them. However those phenomena do not mean malfunction or out of order with LCD's. Which will come back in the specified operating temperature range.
- If the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- A slight dew depositing on terminals is a cause for electro-chemical reaction resulting in terminal open circuit.

Usage under the relative condition of 40°C, 50%RH or less is reequired.

#### (6) Storage

In the case of storing for a long period of time (for instance.) For years) for the purpose or replacement use, The following ways are recommended.

- Storage in a polyethylene bag with sealed so as not to enter fresh air outside in it, And with no desiccant.
- Placing in a dark place where neither exposure to direct sunlight nor light is. Keeping temperature in the specified storage temperature range.
- Storing with no touch on polarizer surface by the anything else. (It is recommended to store them as they have been contained in the inner container at the time of delivery)

#### (7) Safety

- It is recommendable to crash damaged or unnecessary LCD into pieces and wash off liquid crystal by using solvents such as acetone and ethanol. Which should be burned up later.
- When any liquid crystal leaked out of a damaged glass cell comes in contact with your hands, please wash it off well with soap and water.

